



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#15/C
Amend C
Y. Rohina
6/11/03
PATENT

Application No.: 09/779,096) Date: May 29, 2003
Applicant: Shi-Tron Lin, et al.) Examiner: Ori Nadav, Group Art Unit 2811
Filed: February 8, 2001) Our Ref: B-4101, 618582-4
For: "Output Buffer with Good ESD Protection") Re: Preliminary Amendment to RCE

BOX RCE
PO Box 1450
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Sir:

Prior to examination of the above-identified RCE application, it is respectfully requested that the following amendments be made to the claims.

IN THE CLAIMS

Please replace the currently pending Claims 2, 34 and 38 with the amended Claims 2, 34 and 38, which are set forth below. (Appendix A, which is enclosed herewith, shows how these claims were amended.) Please add new claim 39.

2. (Amended) An output buffer, comprising:
a first circuit coupled between a first power line and a pad; and
a second circuit coupled between a second power line and the pad, comprising:
a resistor constructed by a well region of a second conductivity type deposited on a substrate of a first conductivity type, the resistor comprising a first end and a second end, the first end being a doped region of the second conductivity type at least partially overlapping the well region and coupled to the pad;
a first doped region of the first conductivity type, deposited in the well region;
a capacitor coupled between the pad and the first doped region; and
an electrostatic discharge protection component, coupled between the second end and the second power line.

34. (Amended) An electrostatic discharge protection circuit coupled between a first node and a second node, comprising:

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